

LArIAT General News



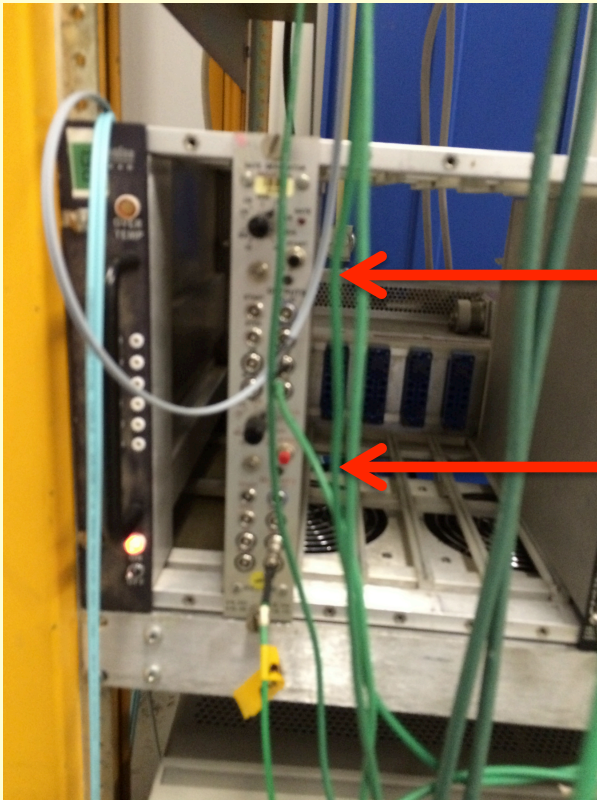
- March 31st beam test date looks likely
- Please route MCenter control room request through me, and enclosure requests through Alan
- A/D people are now actively working now getting ACnet network and TCLK installed in MC-7 and MC-CR
 - (TCLK gives beam clock and spill signals)
- FTBF people arranging for racks to be placed both locations, but please do not use them until we get the OK

LArIAT VME Crates

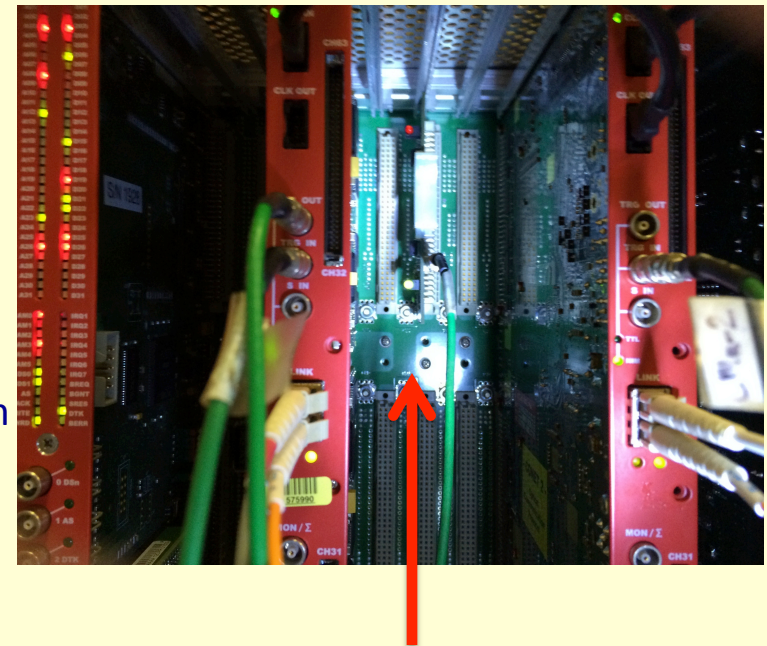


Installed a VME bus reset system

- Currently have gate generator with push buttons for now
- Will set up a programmable CAMAC I/O board for remote reset in MC-7 enclosure



Buttons:
Top to Top Crate
Bottom to Bottom



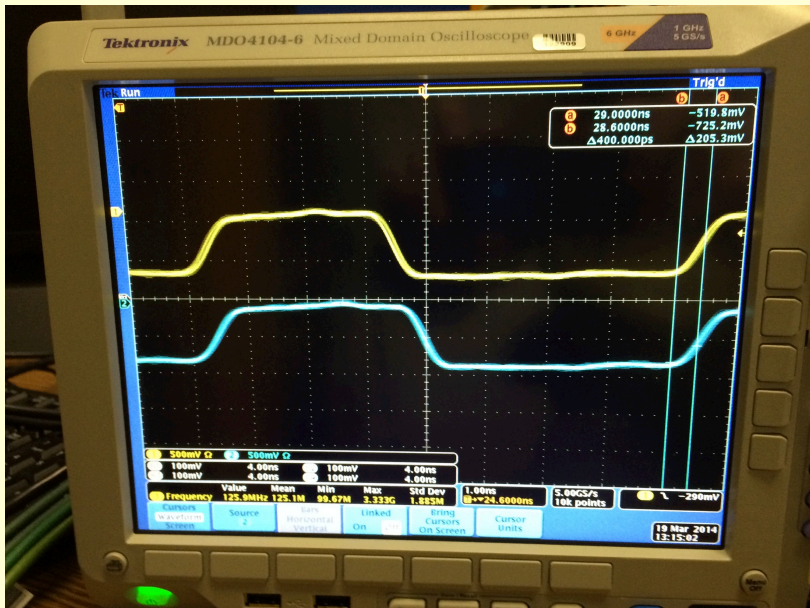
Wee VME Reset Card

LArIAT V1740 Timing



Another iteration with CAEN we now have a working, timed-in chain of eight V1740s

- Clock Master: In/Out 50/62.5, sampling 62.5
- Clock Slaves: In/Out 62.5/62.5, sampling 62.5
- Timing procedure ~ 1.5 hours, will be much faster 2nd time
- Run 1230 has same PMT going to two V1740s, will check



First (Master) versus Last board in chain

Timed better than 1ns

Due to granularity of delay, cannot do better

In “eight in one crate” mode we’ll have to time in again

LArIAT Other DAQ Pending Issues



- Work on Wave Union (Jinyuan) TDC on hold until he returns to fix the word counter problem
- Need to configure and use wave form generator
- Need to implement common start for start of spill
- Waiting for Sten's TDCs to be assembled for Wire Chambers